

Final Call for Papers

HLDVT 2007

IEEE International High-Level Design, Validation and Test Workshop

Hyatt, Irvine, California, Nov. 7–9, 2007

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HLDVT 2007 is the twelfth in a series of annual workshops designed to bring together a community of researchers in the areas of design, validation, and test. The workshop revolves around a common theme of addressing the integration of multiple functions on-chip at higher levels of design abstraction, and the techniques and methodologies for modeling, analyzing, and validating such systems. In particular, the workshop has become a unique forum in recent years for researchers and practitioners to discuss the practical issues associated with simulation and validation of extremely large designs. This year, the theme is **”Challenges in multi-core design, validation and test”**.

Topics of interest include:

- Simulation-Based Validation
- Formal Verification
- Design Abstraction & Behavioral Modeling
- Error Trace Interpretation & Debugging
- Hybrid SAT/BDD/ATPG Methods
- On-Chip and Core-Based Testing
- Test Generation for Defects, Design Errors, and Delay
- Design/Synthesis for Test
- Hardware/Software Co-Validation
- Emulation and Prototyping
- Design from abstract specifications

The Program Committee invites authors to submit papers not to exceed 8 pages (10pt minimum font size, reasonable margins and line spacing) describing original and unpublished work. On the title page, please indicate: paper title, name and affiliations of all authors, and the topic category. Also identify a contact author and provide complete mailing address, phone number, fax number and an e-mail address. Panel proposals are also invited. All submissions must be made electronically in PDF or Postscript format using the paper submission webpage: <http://www.hldvt.com/submissions>

Please ensure that your PDF or Postscript file is readable by Acrobat Reader or Ghostview. The submission of an paper or panel proposal will be considered evidence that upon acceptance, the author(s) will present their paper or organize their panel at the workshop.

Submission deadline: **June 16, 2007**

Final manuscript: **September 13, 2007**

Authors of selected HLDVT’07 papers will be invited to submit extended versions for a special issue of a selected IEEE publication, to be published in 2008.

Additional information is available at <http://www.hldvt.com/07>

Questions regarding paper submissions and the program may be addressed to the program chair: Yatin Hoskote, programchair@hldvt.com. Other questions may be addressed to the general chair: Michael Hsiao, generalchair@hldvt.com.

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