

# Call for Papers

## HLDVT 2009

IEEE International High-Level Design, Validation and Test Workshop  
Grand Hyatt, San Francisco, California, November 4–6, 2009

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HLDVT 2009 is the fourteenth in a series of annual workshops designed to bring together a community of researchers in the areas of design, validation, and test. The workshop revolves around a common theme of addressing the integration of multiple functions on-chip at higher levels of design abstraction, and the techniques and methodologies for modeling, analyzing, and validating such systems. In particular, the workshop has become a unique forum in recent years for researchers and practitioners to discuss the practical issues associated with simulation and validation of extremely large designs. For detailed information, please visit <http://www.hldvt.com/09/>.

Authors of selected HLDVT09 papers will be invited to submit extended versions for a **special issue** of *Springer Journal of Electronic Testing*, to be published in 2010

The topics of interest for HLDVT 2009 include (but not limited to):

- Simulation-Based Validation
- Formal Verification
- Specification-based Design and Validation
- Error Trace Analysis and Debug
- Hybrid SAT/BDD/ATPG Methods
- On-Chip and Core-Based Testing
- Coverage-driven Test Generation
- Design/Synthesis for Test
- Hardware/Software Co-Validation
- Prototyping and Emulation
- Post-silicon Validation and Debug

**Paper Submission:** The Program Committee invites authors to submit papers not to exceed 8 pages (10pt minimum font size with reasonable margins and line spacing) describing original and unpublished work. Proposals for panels and special sessions are also invited. All submissions must be made electronically in PDF format using the paper submission webpage: <http://www.hldvt.com/submissions>

**Extended Submission Deadline:** June 26, 2009 (final and firm deadline)

Acceptance Notification: August 10, 2009 (final manuscript due: Sept 07)

**Paper Publication and Presenter Registration:** During paper submission the authors must commit to register and attend the workshop to present the paper if it is accepted. Every accepted paper must have at least one of the authors pre-registered for the workshop during submission of camera-ready version to ensure that the paper appears in the final program and conference proceedings. *IEEE reserves the right to exclude a paper from distribution after the workshop (e.g., removal from IEEE Xplore) if the paper is not presented at the workshop.*

Questions regarding paper submissions and the program may be addressed to the program chair: Prabhat Mishra, [programchair@hldvt.com](mailto:programchair@hldvt.com). Other questions may be addressed to the general chair: Priyank Kalla, [generalchair@hldvt.com](mailto:generalchair@hldvt.com).

HLDVT 2009 is sponsored by the IEEE Computer Society Test Technology Technical Council and IEEE CS Design Automation Technical Committee.